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*Practice limited to Federal Agencies

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Re: U.S. Utility Patent Application
Application No. 10/780,877; Filing Date: February 19, 2004
For: **Overlay Correction By Reducing Wafer Slipping After Alignment**
Inventor: Peter KOCHERSPERGER
Our Ref: 1857.2140000

Sir:

Transmitted herewith for appropriate action is the following document:

1. Amendment and Reply Under 37 C.F.R. §1.116.

The above-listed document is filed electronically through EFS-Web.

In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

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DJF/A-S:smn
Enclosure

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